IN THE ABSTRACT:

The Abstract as amended below with a replacement Abstract shows added text with underlining and deleted text with strikethrough.

A processor is efficiently controlled and the electric power required to drive the processor is reduced by switching between a process of driving a plurality of arithmetic units using a series of instructions from an instruction control unit and a process of driving a plurality of arithmetic units using a plurality of series of instructions from different instruction control units according to an object to be processed. When a plurality of instruction control units 10, 11 and 12 drive a plurality of arithmetic units 13, 14 and 15, a synchronous execution process of driving the plurality of arithmetic units 13, 14 and 15 using the series of instructions from the first instruction control unit 10 is switched to/from an independent execution process of driving the arithmetic units 13, 14 and 15 using the series of instructions from the instruction control units 10, 11 and 12, respectively, according to the information contained in the series of instructions.